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DKing  
11-22-02

**EXPEDITED PROCEDURE - EXAMINING GROUP 2819**

**S/N 09/896,523**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant:** Steven K. Hsu et al.

**Examiner:** Anh Q. Tran

**Serial No.:** 09/896,523

**Group Art Unit:** 2819

**Filed:** June 29, 2001

**Docket:** 884.453US1

**Title:** VOLTAGE-LEVEL CONVERTER

Entered  
Per RCE  
1-13-03

**AMENDMENT & RESPONSE UNDER 37 C.F.R. § 1.116**

Box AF  
Commissioner for Patents  
Washington, D.C. 20231

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In response to the final office action mailed September 10, 2002, please amend the application as follows.

**IN THE CLAIMS**

Please substitute the claim set in the appendix entitled 'Clean Version of Pending Claims' for the previously pending claim set. The substitute claim set is intended to reflect amendment of previously pending claims 1 and 32. The specific amendments to individual claims are detailed in the following marked up set of claims.

Please amend claims 1 and 32.

1. (Twice.Amended) A voltage-level converter comprising:  
a static voltage-level converter including an inverter coupled to no more than two transistors in the static voltage-level converter each of the no more than two transistors directly coupled to a voltage level; and  
a split-level output circuit coupled to the static voltage-level converter.
  
32. (Amended) The voltage level converter of claim [32] 2, wherein the second transistor of the first pair of transistors and the second transistor of the second pair of transistors are insulated gate field-effect transistors.